AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Yuichiro YAMADA, et al.					Docket No. L8462.01108A	
Serial No. 10/616,981	July	g Date ANS 3 2004	J. Clark		Group Art Unit 2815	
Invention: SEMICONDUCTOR CHIP, WIRING BOARDENAD MANUFACTURING PROCESS THEREOF AS WELL AS SEMICONDUCTOR DEVICE						
TO THE COMMISSIONER FOR PATENTS:						
Transmitted herewith is an amendment in the above-identified application.						
The fee has been calculated and is transmitted as shown below.  CLAIMS AS AMENDED						
	CLAIMS REMAINING	HIGHEST #	NUMBER EXTRA  CLAIMS PRESENT	RATE	ADDITIONAL	
TOTAL CLAIMS	AFTER AMENDMENT  14 -	PREV. PAID FOR 42 =	CLAIMS PRESENT	x \$18.	.00 FEE	
INDEP. CLAIMS	8 -	15 =	0	x \$86.		
Multiple Dependent Claims (check if applicable)					\$0.00	
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT \$0.00						
No additional fee is required for amendment.  Please charge Deposit Account No. in the amount of  A check in the amount of to cover the filing fee is enclosed.  The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No.  Any additional filing fees required under 37 C.F.R. 1.16.  Any patent application processing fees under 37 CFR 1.17.						
James E. Ledbetter, Esq. Registration No. 28,732 Stevens, Davis, Miller & Mosher, LLP 1615 L Street, N.W., Suite 850 Washington, DC 20036 Tel: 202-785-0100 Fax: 202-408-5200  Dated: August 30, 2004    Certify that this document and fee is being deposited on with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.    Signature of Person Mailing Correspondence						
cc:				Typed or Printed Name of Person Mailing Correspondence		



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yuichiro YAMADA, et al. Art Unit: 2815 Inventors:

Serial No.:

10/616,981

Examiner: J. Clark

Filed:

July 11, 2003

For:

SEMICONDUCTOR CHIP, WIRING BOARD AND MANUFACTURING

PROCESS THEREOF AS WELL AS SEMICONDUCTOR DEVICE

## AMENDMENT AND RESPONSE TO QUAYLE ACTION

Assistant Commissioner of Patents Washington, DC 20231

Sir:

In response to the office action of June 29, 2004, please amend the above-captioned application as follows: